

Fig. 1

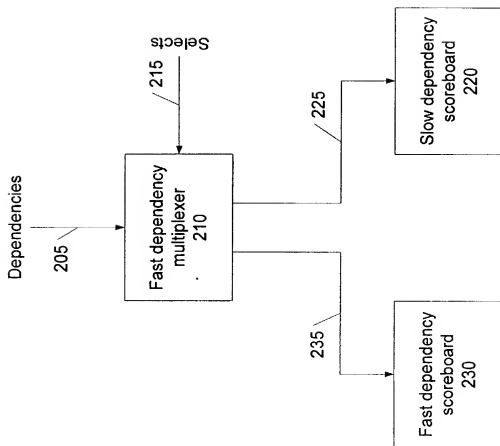
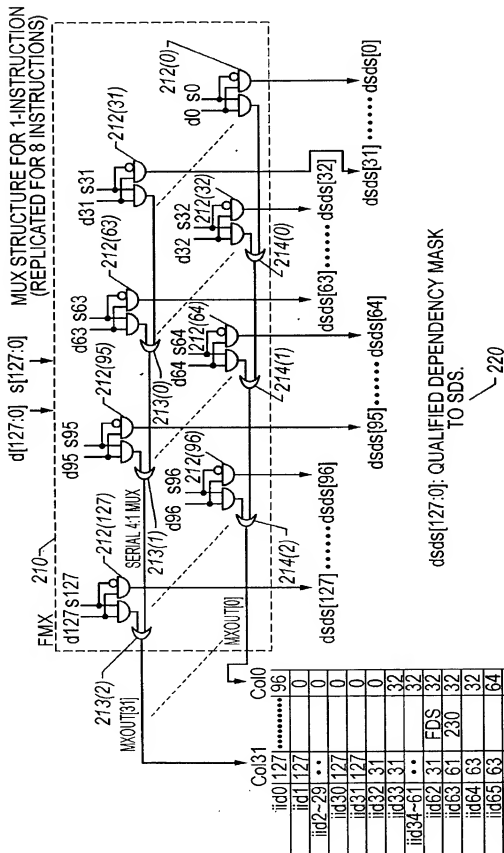


Fig. 2A



300

Instruction Ds of the first  
instruction of each octet

128 bits

Octet	127:120	119:112	111:104	103:96	95:88	87:80	79:72	71:64	63:56	55:48	47:40	39:32	31:24	23:16	15:8	7:0
0	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
1	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
2	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
3	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
4	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
5	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
6	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
7	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
8	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
9	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
10	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
11	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
12	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
13	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
14	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000
15	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000	000000

Fig. 3A

10041763 - 050002

FAST INSTRUCTION DEPENDENCY MULTIPLEXER

[illegible]

**Write Pointer = 60      “Current Octet Pattern”**

[illegible]

### Write Pointer = 60 “Next Octet Pattern”

**Fig 3B**

10091733, 052002

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March 6, 2002

FAST INSTRUCTION DEPENDENCY MULTIPLEXER

[illegible]

### Single strand mode

**Write Pointer = 60 “Final Select Pattern”**

**Fig 3C**

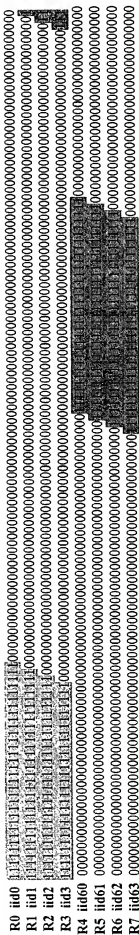
**Write Pointer = 60    “Current Octet Pattern”    Multi-strand mode**

Write Pointer = 60      “Next Octet Pattern”      Multi-strand mode

**Fig 4A**

11091782, 057000

March 6, 2002  
FAST INSTRUCTION DEPENDENCY MULTIPLEXER



**Write Pointer = 60      "Final Select Pattern"      Multi-strand mode**

**Fig 4B**